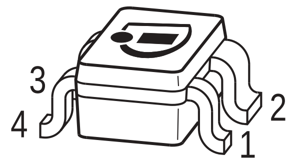
NPN Silicon RF Transistor



- For medium power amplifiers
- Compression point P_{-1dB} = +19 dBm at 1.8 GHz
  maximum available gain G_{ma} = 15.5 dB at 1.8 GHz
  Noise figure F = 1.25 dB at 1.8 GHz
- Transition frequency f_T = 24 GHz
- Gold metallization for high reliability
- SIEGET ® 25 GHz fT - Line
- Pb-free (RoHS compliant) package[1)]
- Qualified according AEC Q101

ESD (**E**lectro**s**tatic **d**ischarge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration						Package
BFP450	ANs	1=B	2=E	3=C	4=E	-	-	SOT343

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}		V
T_A > 0 °C		4.5	
$T_A \leq$ 0 °C		4.1	
Collector-emitter voltage	V_{CES}	15	
Collector-base voltage	V_{CBO}	15	
Emitter-base voltage	V_{EBO}	1.5	
Collector current	I_C	100	mA
Base current	I_B	10	
Total power dissipation[2)]	P_{tot}	450	mW
$T_S \leq$ 96 °C			
Junction temperature	T_j	150	°C
Ambient temperature	T_A	-65 ... 150	
Storage temperature	T_{stg}	-65 ... 150	

[1]Pb-containing package may be available upon special request
[2]T_S is measured on the collector lead at the soldering point to the pcb

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Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point[1)]	R_{thJS}	$\leq$ 120	K/W

Electrical Characteristics at T_A = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage I_C = 1 mA, I_B = 0	$V_{(BR)CEO}$	4.5	5	-	V
Collector-emitter cutoff current V_{CE} = 15 V, V_{BE} = 0	I_{CES}	-	-	10	µA
Collector-base cutoff current V_{CB} = 5 V, I_E = 0	I_{CBO}	-	-	100	nA
Emitter-base cutoff current V_{EB} = 0.5 V, I_C = 0	I_{EBO}	-	-	10	µA
DC current gain I_C = 50 mA, V_{CE} = 4 V, pulse measured	h_{FE}	60	95	130	-

[1]For calculation of R_{thJA} please refer to Application Note Thermal Resistance

Electrical Characteristics at T_A = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency I_C = 90 mA, V_{CE} = 3 V, f = 1 GHz	f_T	18	24	-	GHz
Collector-base capacitance V_{CB} = 2 V, f = 1 MHz, V_{BE} = 0 , emitter grounded	C_{cb}	-	0.48	0.8	pF
Collector emitter capacitance V_{CE} = 2 V, f = 1 MHz, V_{BE} = 0 , base greunded	C_{ce}	-	1.2	-	
Emitter-base capacitance V_{EB} = 0.5 V, f = 1 MHz, V_{CB} = 0 , collector grounded	C_{eb}	-	1.75	-	
Noise figure I_C = 10 mA, V_{CE} = 2 V, f = 1.8 GHz, $Z_S = Z_{Sopt}$	F	-	1.25	-	dB
Power gain, maximum available[1)] I_C = 50 mA, V_{CE} = 2 V, $Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$, f = 1.8 GHz	G_{ma}	-	15.5	-	
Insertion power gain V_{CE} = 2 V, I_C = 50 mA, f = 1.8 GHz, $Z_S = Z_L$ = 50 Ω	$\|S_{21}\|^2$	8	11.5	-	dB
Third order intercept point at output[2)] V_{CE} = 3 V, I_C = 50 mA, f = 1.8 GHz, $Z_S = Z_L$ = 50 Ω	IP_3	-	29	-	dBm
1dB Compression point at output I_C = 50 mA, V_{CE} = 3 V, $Z_S = Z_L$ = 50 Ω, f = 1.8 GHz	P_{-1dB}	-	19	-	

[1]$G_{ma} = |S_{21e} / S_{12e}| (k-(k^2-1)^{1/2})$

[2]IP3 value depends on termination of all intermodulation frequency components.
Termination used for this measurement is 50Ω from 0.1 MHz to 6 GHz

SPICE Parameter (Gummel-Poon Model, Berkley-SPICE 2G.6 Syntax):

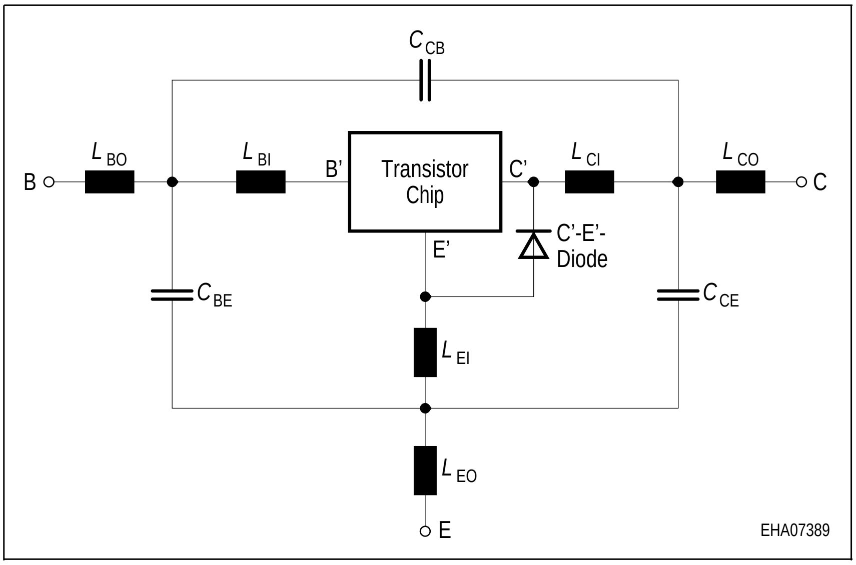
Transistor Chip Data:

IS =	0.13125	fA	BF =	76.123	-	NF =	0.79652	-
VAF =	24.165	V	IKF =	0.58905	A	ISE =	28341	fA
NE =	1.5563	-	BR =	21.254	-	NR =	1.2966	-
VAR =	13.461	V	IKR =	0.25878	A	ISC =	0.012292	fA
NC =	0.70543	-	RB =	5.403	Ω	IRB =	0.013181	mA
RBM =	2.1659	Ω	RE =	0.45346	-	RC =	0.50084	Ω
CJE =	3.2276	fF	VJE =	0.95292	V	MJE =	0.48672	-
TF =	7.5068	ps	XTF =	0.69972	-	VTF =	0.66148	V
ITF =	0.017655	mA	PTF =	0	deg	CJC =	1049.5	fF
VJC =	1.1487	V	MJC =	0.50644	-	XCJC =	0.28285	-
TR =	2.6912	ns	CJS =	0	F	VJS =	0.75	V
MJS =	0	-	XTB =	0	-	EG =	1.11	eV
XTI =	3	-	FC =	0.91274		TNOM	300	K

C`-E`-dioden Data (Berkley-Spice 1G.6 Syntax): IS = 25 fA; N = 1.05 -, RS = 5 Ω

All parameters are ready to use, no scalling is necessary.

Package Equivalent Circuit:



L_{BI} =	0.31	nH
L_{BO} =	0.63	nH
L_{EI}=	0.2	nH
L_{EO} =	0.05	nH
L_{CI} =	0.29	nH
L_{CO} =	0.68	nH
C_{BE} =	208	fF
C_{CB} =	3.2	fF
C_{CE} =	213	fF

Valid up to 6GHz

The SOT343 package has two emitter leads. To avoid high complexity to the package equivalent circuit both leads are combined in one electrical connection

Extracted on behalf of Infineon Technologies AG by: Institut für Mobil- und Satellitentechnik (IMST)

For examples and ready to use parameters please contact your local Infineon Technologies distributor or sales office to obtain a InfineonTechnologies CD-ROM or see Internet: http//www.infineon.com/silicondiscretes

For non-linear simulation:

· Use transistor chip parameters in Berkeley SPICE 2G.6 syntax for all simulators.

· If you need simulation of the reverse characteristics, add the diode with the C'-E'- diode data between collector and emitter.

· Simulation of package is not necessary for frequencies < 100MHz. For higher frequencies add the wiring of package equivalent circuit around the non-linear transistor and diode model.

Note:

· This transistor is constructed in a common emitter configuration. This feature causes an additional reverse biased diode between emitter and collector, which does not effect normal operation.

Transistor Schematic Diagram

The common emitter configuration shows the following advantages:

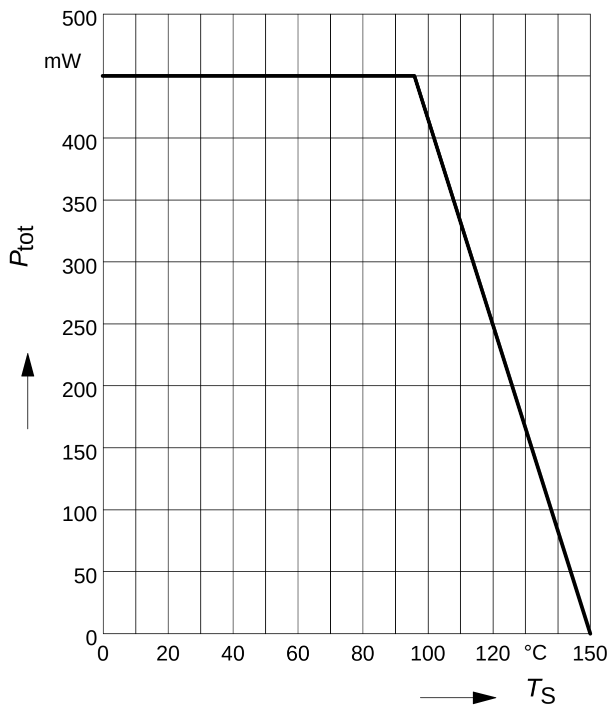
· Higher gain because of lower emitter inductance.
· Power is dissipated via the grounded emitter leads, because the chip is mounted on copper emitter leadframe.

Please note, that the broadest lead is the emitter lead.

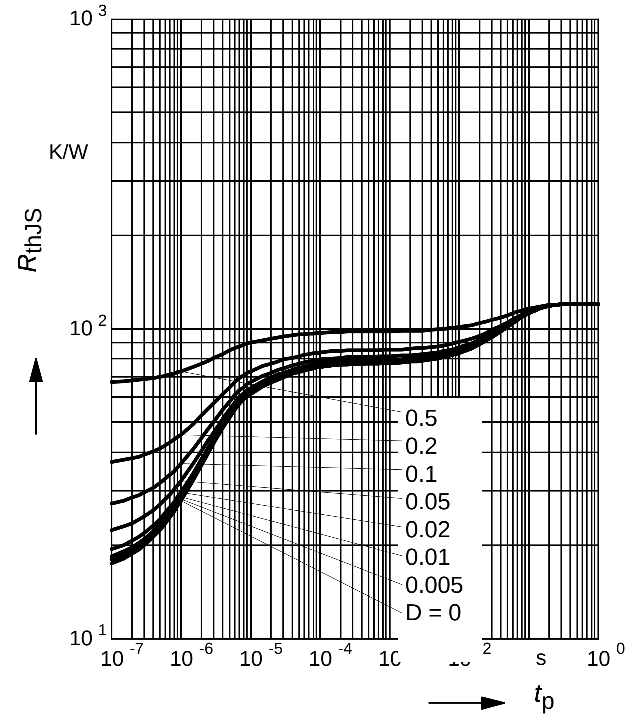
Common Emitter S- and Noise-parameter

For detailed S- and Noise-parameters please contact your local Infineon Technologies distributor or sales office to obtain a Infineon Technologies Application Notes CD-ROM or see Internet: http://www.infineon.com/silicondiscretes

Total power dissipation $P_{tot} = f(T_S)$

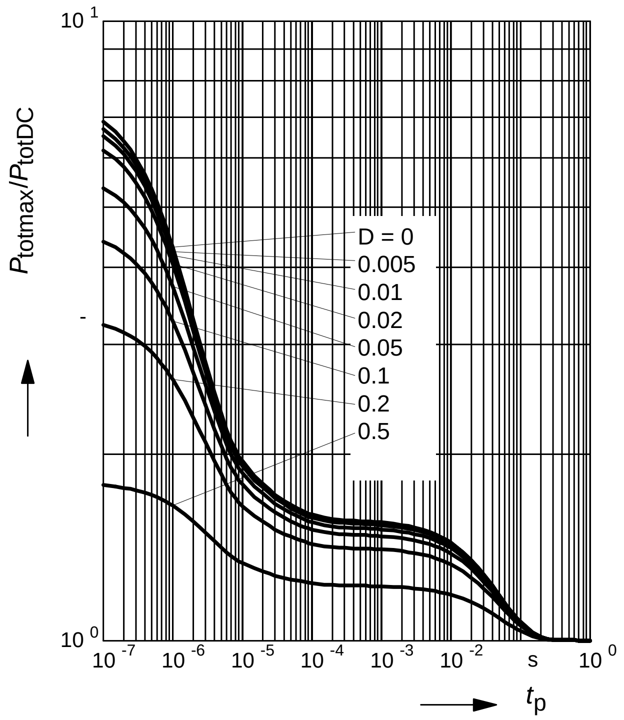


Permissible Pulse Load $R_{thJS} = f(t_p)$



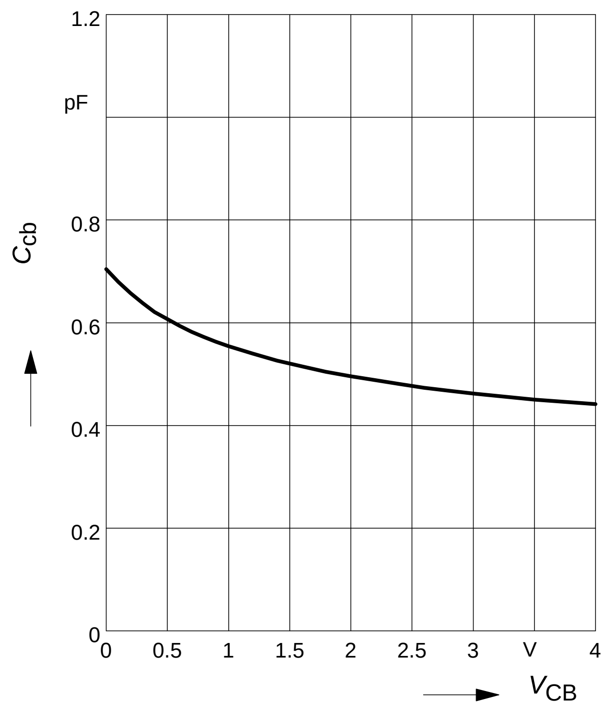
Permissible Pulse Load

$P_{totmax}/P_{totDC} = f(t_p)$



Collector-base capacitance $C_{cb} = f(V_{CB})$

f = 1MHz



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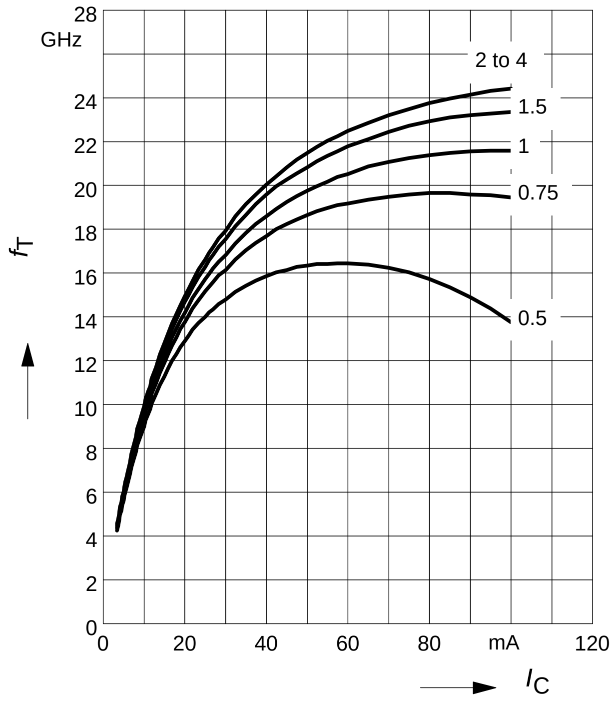
Transition frequency $f_T = f(I_C)$

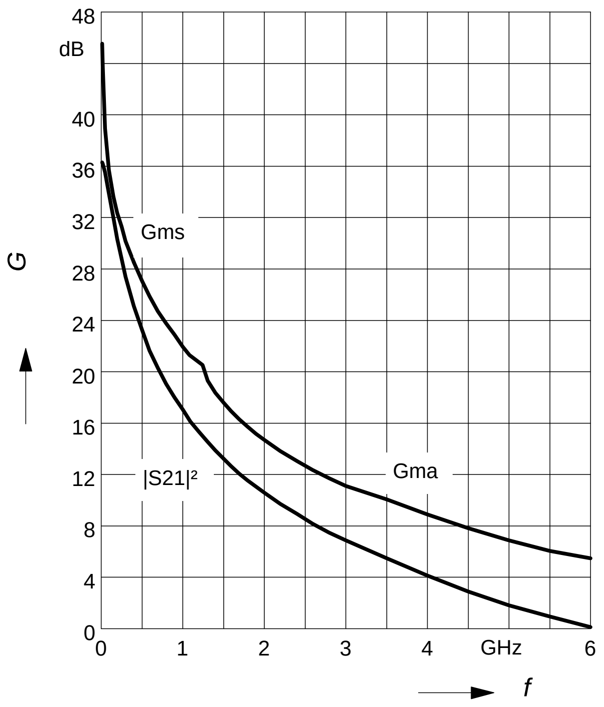
f = 1 GHz

V_{CE} = parameter in V



Power gain G_{ma}, G_{ms}, $|S_{21}|^2 = f(f)$

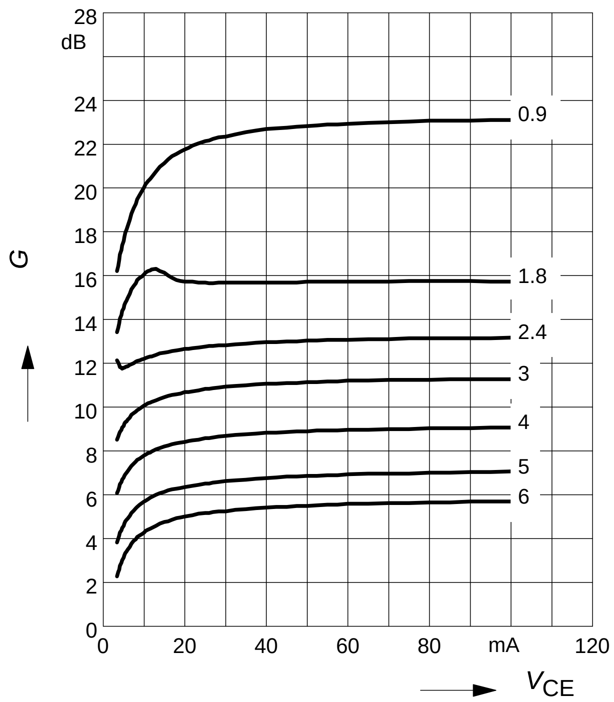
V_{CE} = 2 V, I_C = 50 mA



Power gain G_{ma}, $G_{ms} = f(I_C)$

V_{CE} = 2V

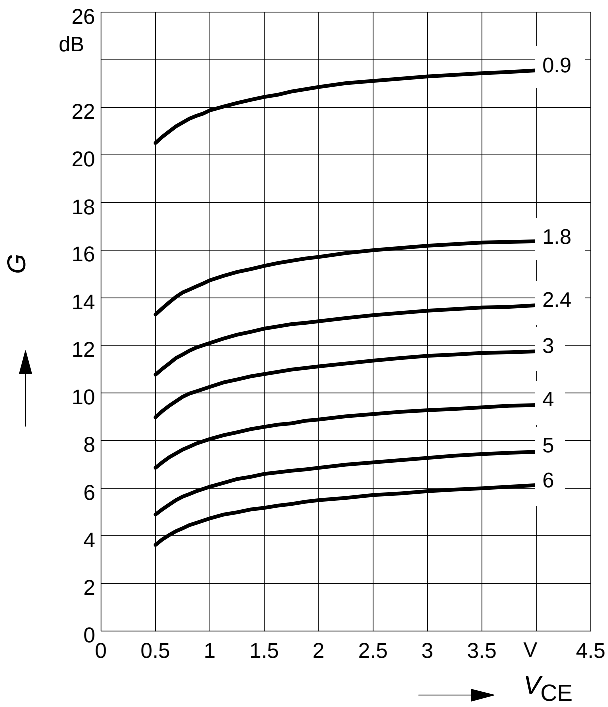
f = parameter in GHz



Power gain G_{ma}, $G_{ms} = f(V_{CE})$

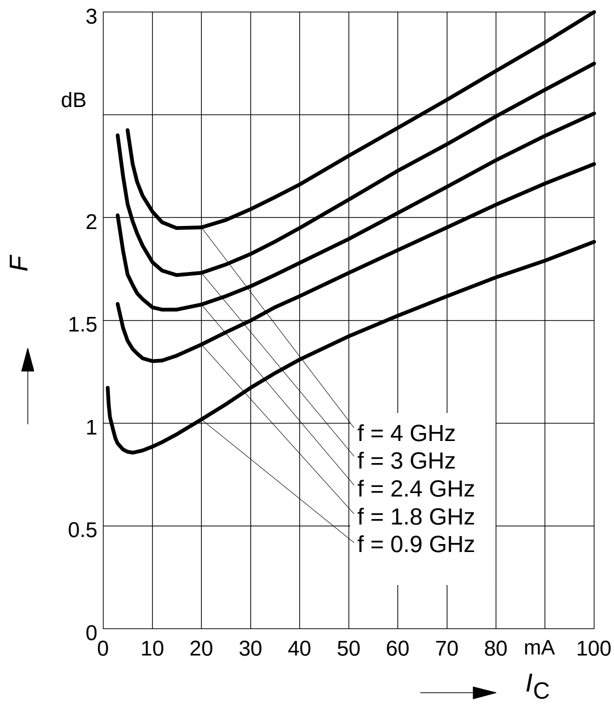
I_C = 50 mA

f = parameter in GHz



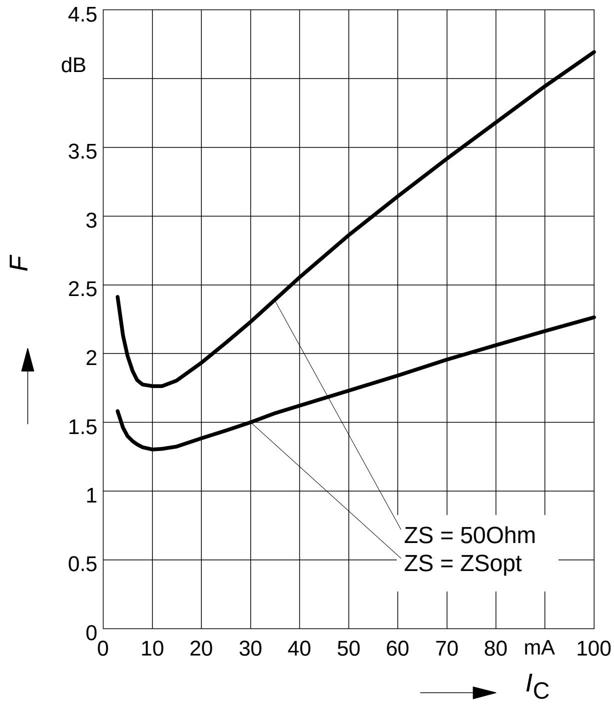
Noise figure $F = f(I_C)$

V_{CE} = 2 V, $Z_S = Z_{Sopt}$



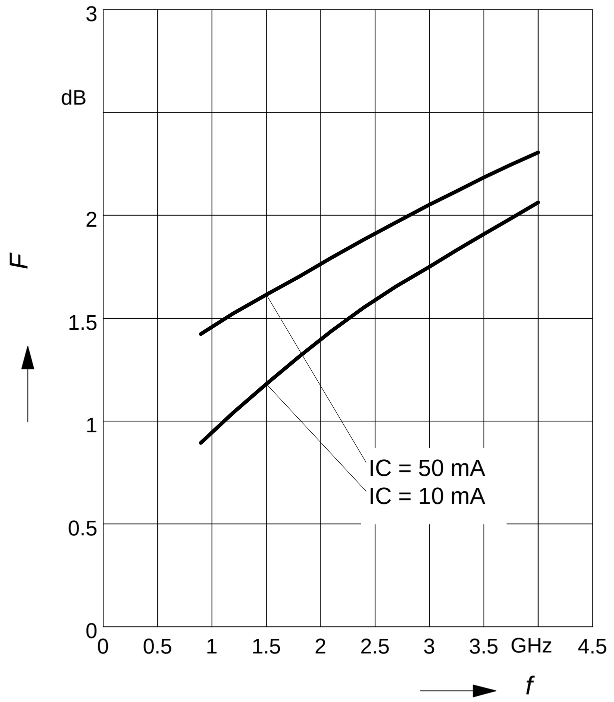
Noise figure $F = f(I_C)$

V_{CE} = 2 V, f = 1.8 GHz



Noise figure $F = f(f)$

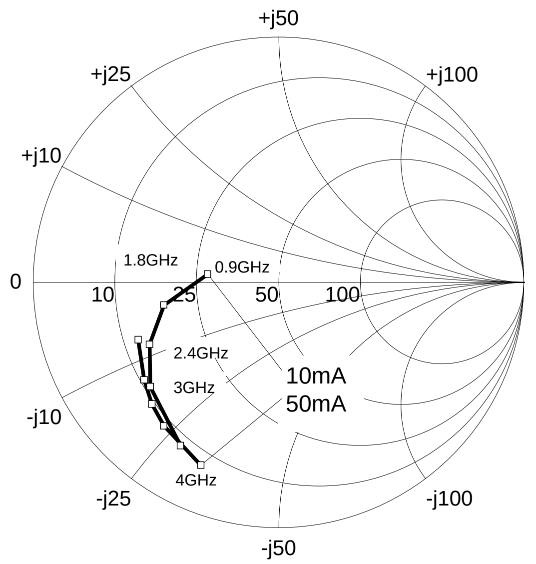
V_{CE} = 2 V, $Z_S = Z_{Sopt}$



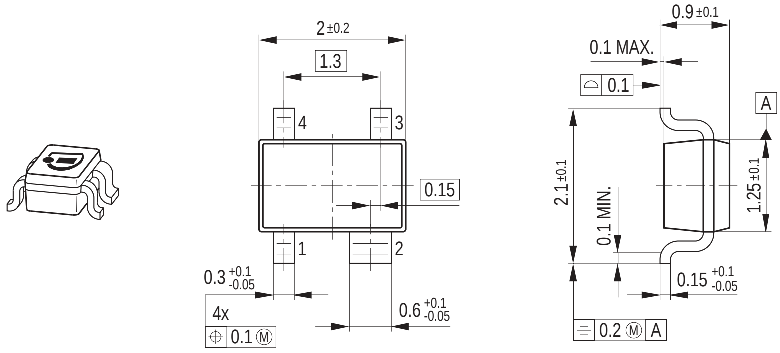
Source impedance for min.

noise figure vs. frequency

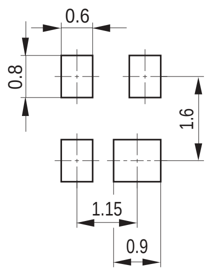
V_{CE} = 2 V, I_C = 10mA / 50 mA



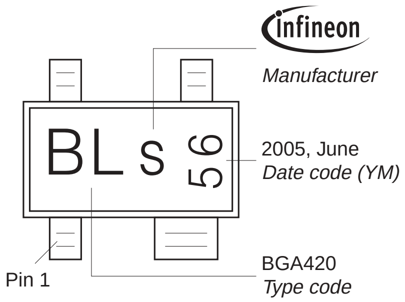
Package Outline



Foot Print

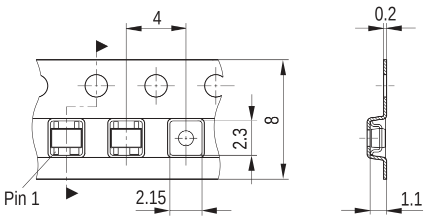


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel
Reel ø330 mm = 10.000 Pieces/Reel



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